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March 12, 2002

Date

Denise Sheridan

Denise Sheridan

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Leland R. Nevill

Attorney Docket No.: 500060.02

Serial No. : 09/998,594

Group Art Unit : 2876

Filed : November 16, 2001

Title : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS

**REQUEST FOR CORRECTED FILING RECEIPT**

Office of Initial Patent Examination  
Customer Corrections Branch  
Commissioner of Patents  
Washington, D.C. 20231

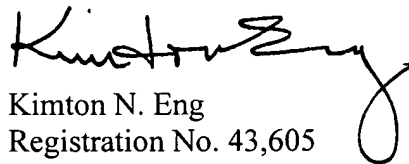
Sir:

Attached is a copy of the official Filing Receipt received from the PTO in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error with respect to the following data, which is incorrectly entered. There is an error in the Title section, which should read METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS.

The correction to be made has been marked in red on the copy of the enclosed Filing Receipt.

Respectfully submitted,  
DORSEY & WHITNEY LLP

  
Kimton N. Eng  
Registration No. 43,605

Enclosures:

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Copy of Filing Receipt



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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY DOCKET NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|---------------|----------------|----------|------------|------------|
| 09/998,594         | 11/16/2001  | 2876         | 0.00          | 500060.02      | 1        | 34         | 8          |

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CONFIRMATION NO. 6204

## FILING RECEIPT



\*OC000000007477088\*

Edward W. Bulchis  
Dorsey & Whitney LLP  
1420 Fifth Avenue  
Suite 3400  
Seattle, WA 98101-4010



Date Mailed: 02/14/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

## Applicant(s)

Leland R. Nevill, Boise, ID;

## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A REI OF 08/857,100 05/15/1997 PAT 5,984,190

## Foreign Applications

If Required, Foreign Filing License Granted 02/14/2002

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

## Title

Method and apparatus for identifying integrated circuits

**integrated**

## Preliminary Class

235

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**LICENSE FOR FOREIGN FILING UNDER**  
**Title 35, United States Code, Section 184**  
**Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Bib Data Sheet

CONFIRMATION NO. 6204.

|  |   |                                |   |   |
|--|---|--------------------------------|---|---|
| <b>SERIAL NUMBER</b><br>09/998,594   | <b>FILING DATE</b><br>11/16/2001<br><b>RULE</b>   | <b>CLASS</b><br>235            | <b>GROUP ART UNIT</b><br>2876   | <b>ATTORNEY DOCKET NO.</b><br>500060.02 |
| <b>APPLICANTS</b><br>Leland R. Nevill, Boise, ID;  |   |                                |   |   |
| <b>** CONTINUING DATA *****</b><br>THIS APPLICATION IS A REI OF 08/857,100 05/15/1997 PAT 5,984,190 <i>DS</i><br><i>yes</i>  |   |                                |   |   |
| <b>** FOREIGN APPLICATIONS *****</b> <i>DS</i><br><i>None</i>  |   |                                |   |   |
| <b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b><br><b>** 02/14/2002</b>   |   |                                |   |   |
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br>35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance |   | <b>STATE OR COUNTRY</b><br>ID  | <b>SHEETS DRAWING</b><br>1  | <b>TOTAL CLAIMS</b><br>34               |
| Verified and Acknowledged<br>Examiner's Signature <i>Dorsey</i> Initials <i>DS</i>   |   | <b>INDEPENDENT CLAIMS</b><br>8 |   |   |
| <b>ADDRESS</b><br>Edward W. Bulchis<br>Dorsey & Whitney LLp<br>1420 Fifth Avenue<br>Suite 3400<br>Seattle ,WA 98101-4010   |   |                                |   |   |
| <b>TITLE</b><br>Method and apparatus for identifying integrated circuits   |   |                                |   |   |
| <b>FILING FEE RECEIVED</b><br>0.00   | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: |                                | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )<br><input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____<br><input type="checkbox"/> Credit |   |

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